



TH5C-11S

Eureka Multi-Function Digital Timer

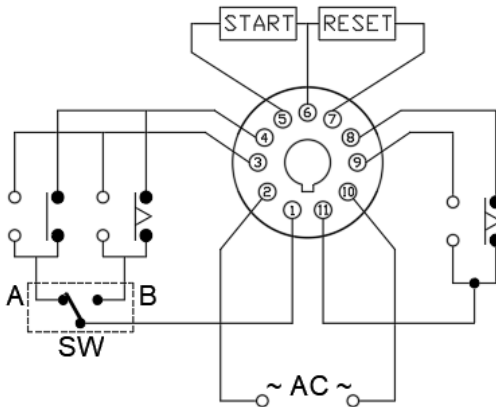
- 11 Operating Modes
- Wide time ranges from 0.001 seconds to 9999 hours
- Status Display; elapsed time, remaining time.
- Memory Retention capability
- Tamper proof (Key Protect); TH5C-11S only
- All functions are field selectable via front panel
- All input signals are opto-isolated from AC power input
- 12VDC / 50mA insulated power source for external DC sensor (TH5C-11S only)



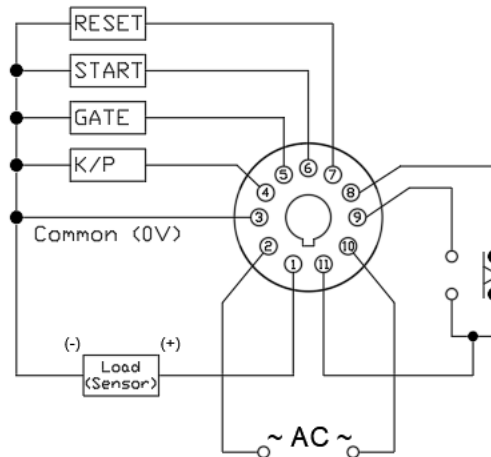
Properties

Properties		
	TH5C-11D	TH5C-11S
Manufacturer	Eureka	
Product Range	Eureka Multi-Function Digital Timers	
Time Range	0.001 seconds to 9999 hours	
Rated Supply Voltage	100 to 240 VAC	
Operating Voltage Range	85% to 110% of rated voltage	
Rated Frequency	50 / 60Hz	
Contact Rating	250 VAC 5A (resistive load)	
Reset Time	0.1s max	
Power Consumption	Approximately 2 VA max	
Endurance	Mechanical	5000000 Operations
	Electrical	100000 Operations
Pin Configuration	11 pins	11 Pins
Ambient Temperature	-10°C~+50°C (No Ice)	
Ambient Humidity	85% RH max	
Panel Cut-Out	45 x 45mm	
Output Contacts	2C 1A1C	1C
Available Features	Memory	Memory
	External Reset	External Reset
	External Start	External Start
		External Gate Key Protect (K/P)
Available Modes	A : Signal ON Delay	A : Signal ON Delay
	A1: Signal ON Delay2	A1: Signal ON Delay2
	A2: Power ON Delay	A2: Power ON Delay
	A3: Signal ON Delay	A3: Signal ON Delay
	B : Repeat Cycle 1	B : Repeat Cycle 1
	B1: Repeat Cycle 2	B1: Repeat Cycle 2
	B2: Repeat Cycle ON Start	B2: Repeat Cycle ON Start
Accessories	C : Signal ON/OFF Delay	C : Signal ON/OFF Delay
	D : Signal OFF Delay	D : Signal OFF Delay
	E : Interval	E : Interval
	F : Cumulative	F : Cumulative
	P2CF-11; Round 11 pin base with clips	
P3G-11; 11 pin base back wired		
Y48; Panel Mount Bracket		

Wiring Diagram for TH5C-11D

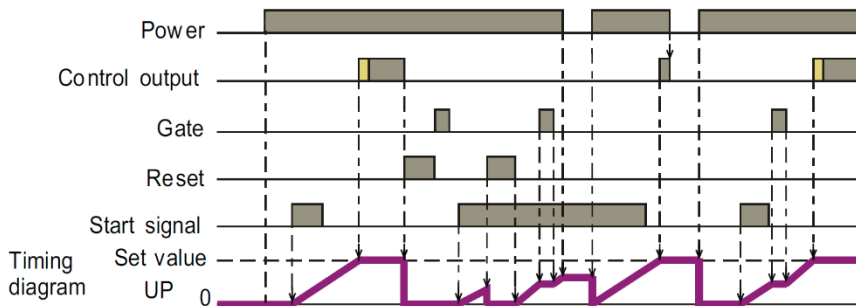


Wiring Diagram for TH5C-11S



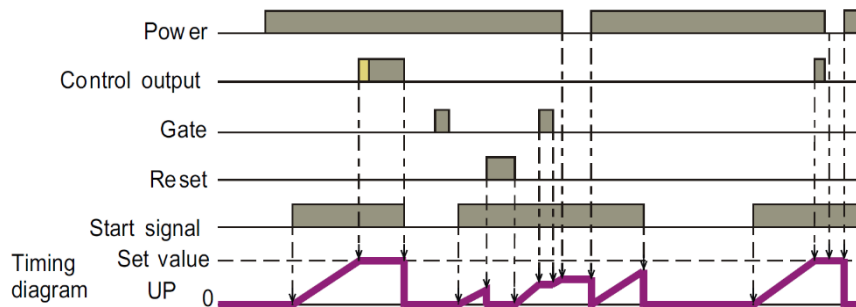
Timing Chart for TH5C

Mode A:
Signal ON Delay
(Timer resets when power comes on)



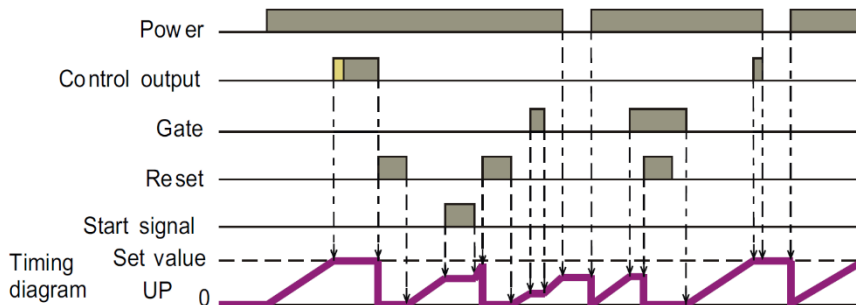
Timing starts when the start signal goes ON. While the start signal is ON, the timer starts when the power comes ON or when the reset input goes OFF. The control LED using a sustained or one-shot time period.

Mode A1:
Signal ON Delay2
(Timer resets when power comes on or when START signal goes off)



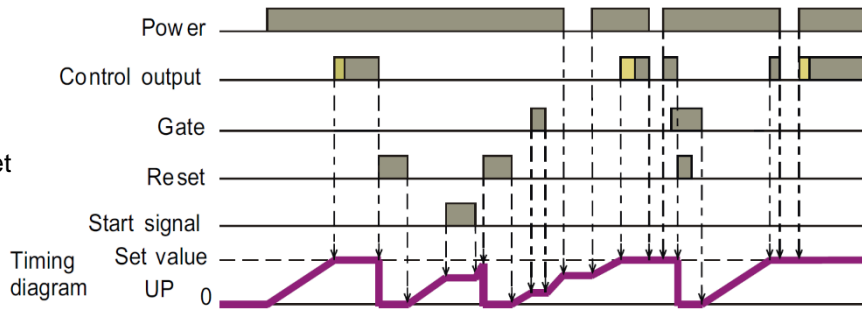
Timing starts when the start signal goes ON and is reset when the start signal goes OFF. While the start is ON, the timer starts when the power comes ON or when the reset input goes OFF. The control output is control LED using a sustained or one-shot time period.

Mode A2:
Power ON Delay
(Timer resets when power comes on)



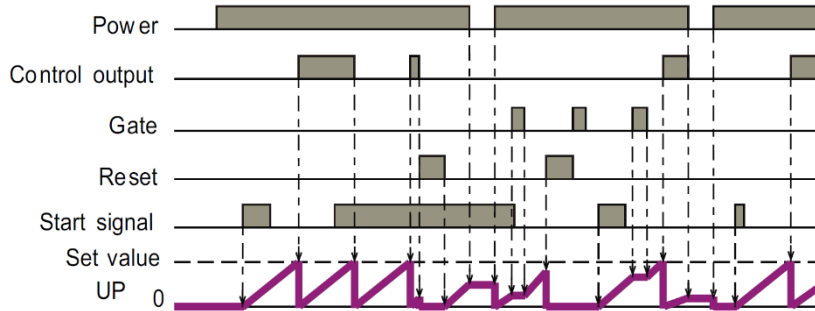
Timing starts when the start signal goes ON. The start signal disables the timing function (i.e., same function as the gate input) The control output is control LED using a sustained or one-shot time period.

Mode A3:
Signal ON Delay
(Timer does not reset when power comes on)



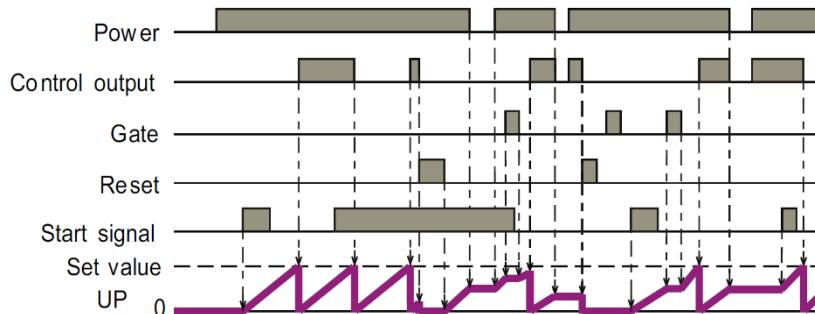
Timing starts when the start signal goes ON. The start signal disables the timing function (i.e. , same function as the gate input) The control output is control LED using a sustained or one-shot time period.

Mode B:
Repeat at Cycle 1
(Timer resets when power comes on)
Output can be sustained or one-shot



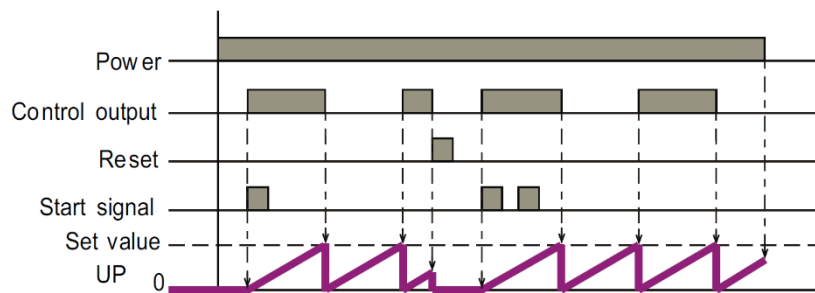
Timing starts when the start signal goes ON. The status of the control output is reversed when time is up (OFF at start) While the start signal is ON, the timer starts when the power comes ON or when the reset input goes OFF.

Mode B1:
Repeat at Cycle 2
(Timer does not reset when power comes on) Output can be sustained or one-shot



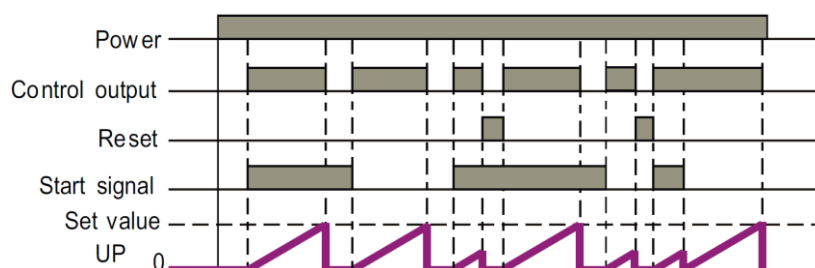
Timing starts when the start signal goes ON. The status of the control output is reversed when time is up (OFF at start) While the start signal is ON, the timer starts when the power comes ON or when the reset input goes OFF.

Mode B2:
Repeat at Cycle 2
(Timer does not reset when power comes on) Output can be sustained or one-shot



Timing starts when the start signal goes ON. The status of the control output is reversed when time is up (OFF at start) While the start signal is ON, the timer starts when the power comes ON or when the reset input goes OFF.

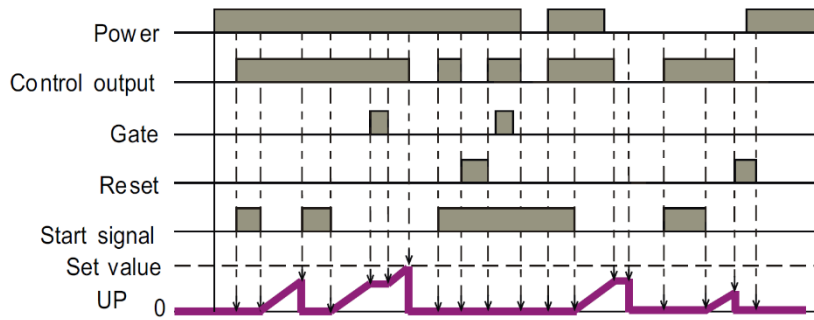
Mode C:
Signal ON/OFF delay
(Timer reset when power comes on)



Timing starts when the start signal goes ON or OFF. The status of the control output is ON when the start signal goes ON or OFF.

Mode D:

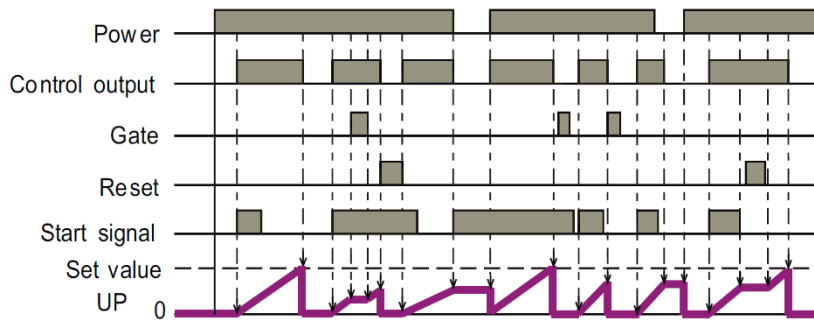
Signal OFF delay
(Timer reset when power comes on)



The control Output is ON when the start signal is ON (except when the power is OFF, or the reset is ON).

Mode E:

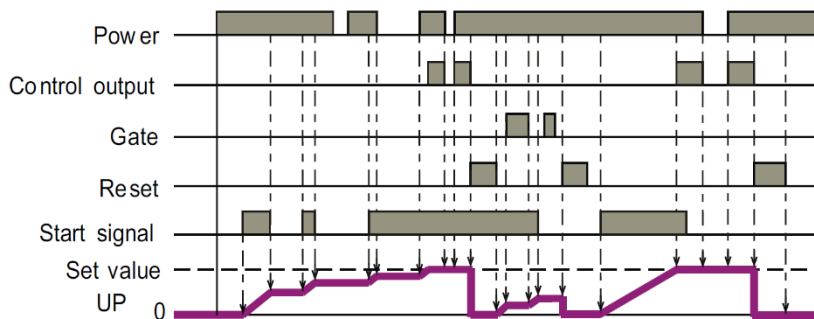
Interval
(Timer reset when power comes on)



Timing starts when the start signal; comes ON. The control output is reset when time is up. While the start signal is ON, the timer starts when the power comes ON or when the reset input goes OFF.

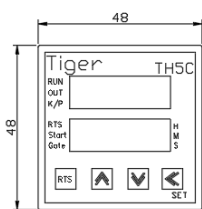
Mode E:

Cumulative
(Timer does not reset when power comes ON.)

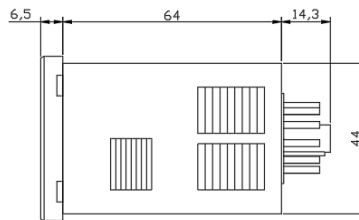


Start signal enables timing (timing is stopped when the start signal is OFF or when the power is OFF) A sustained control output is used.

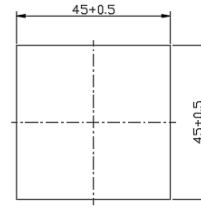
Dimension for TH5C-□□



FRONT VIEW

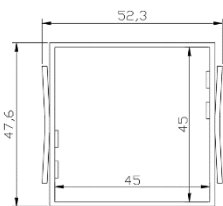


SIDE VIEW

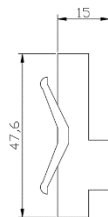


PANEL MOUNT CUT-OUT

Dimension for Y48

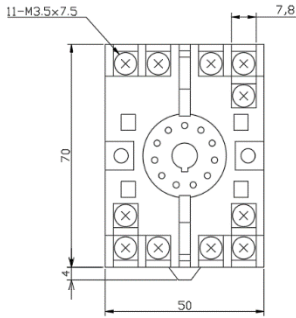


FRONT VIEW

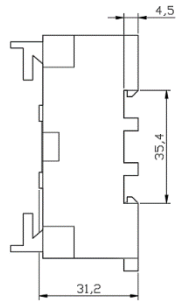


SIDE VIEW

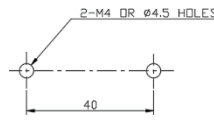
Dimension for P2CF-11



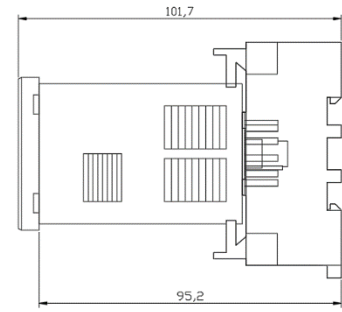
FRONT VIEW



SIDE VIEW

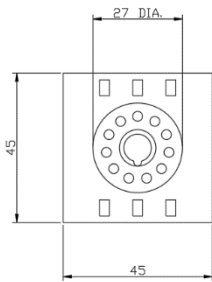


MOUNTING HOLE

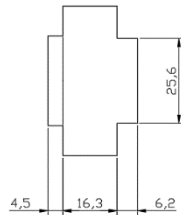


TH5C-xx + P2CF-11

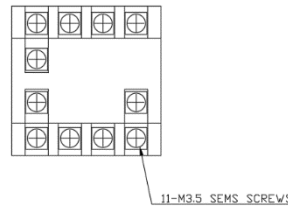
Dimension for P3G-11



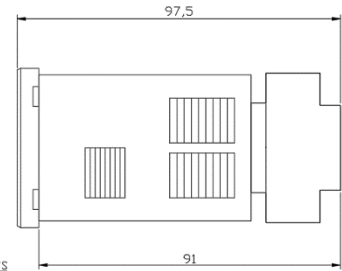
FRONT VIEW



SIDE VIEW



MOUNTING HOLE



TH5C-xx + P3G-11